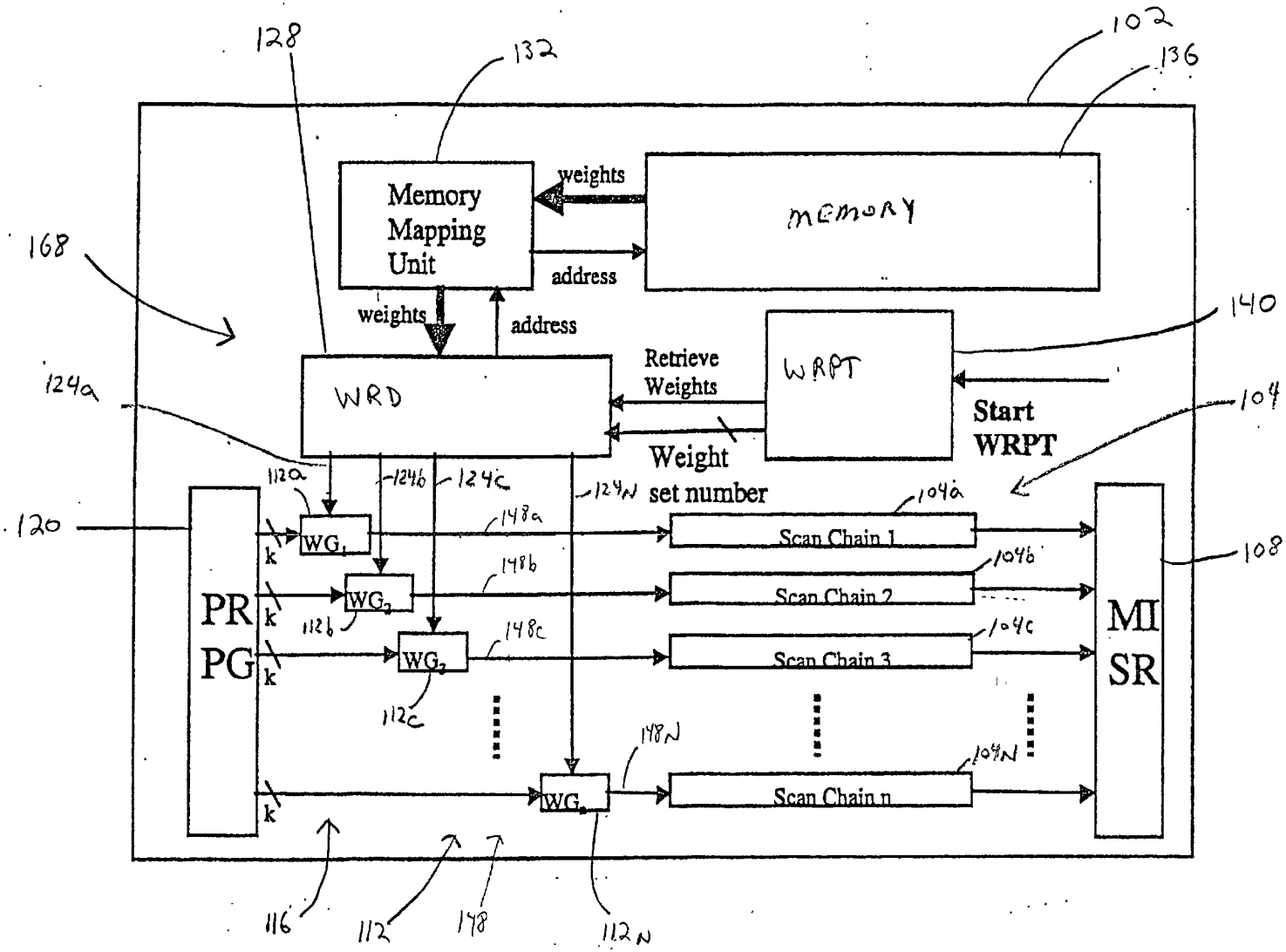
128
132
102
136
Memory Mapping Unit
weights
address
memory
168
weights
address
140
WRD
Retrieve Weights
WRPT
Start WRPT
124a
104
Weight set number
112a
124b
124c
124N
120
148a
104a
Scan Chain 1
108
148b
104b
Scan Chain 2
PR
PG
k
112b
148c
104c
Scan Chain 3
MI
SR
112c
148N
104N
Scan Chain n
116
112
148
112N

FIGURE 2

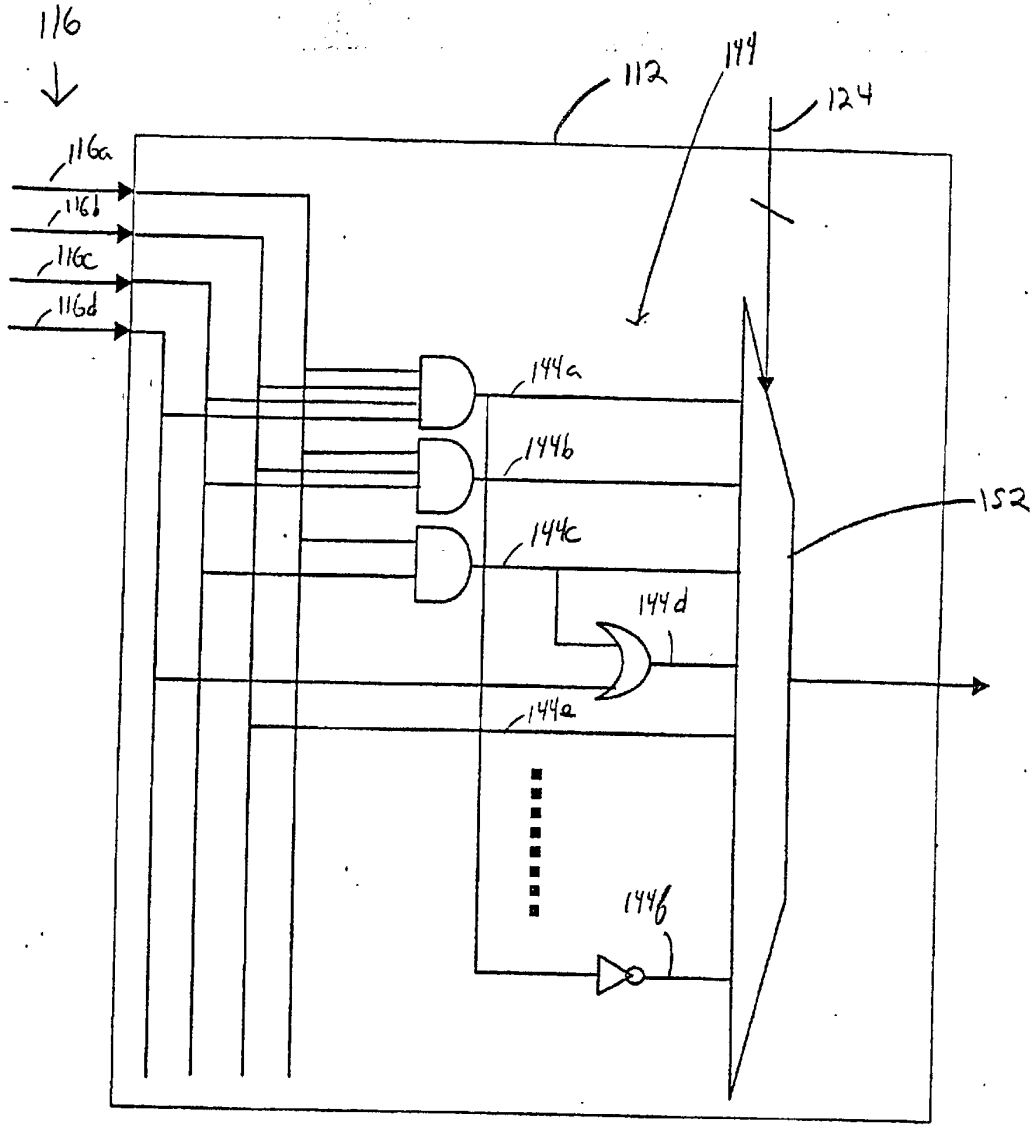
116
112
149
124
116a
116b
116c
116d
144a
144b
144c
144d
144e
144f
152

FIGURE 2

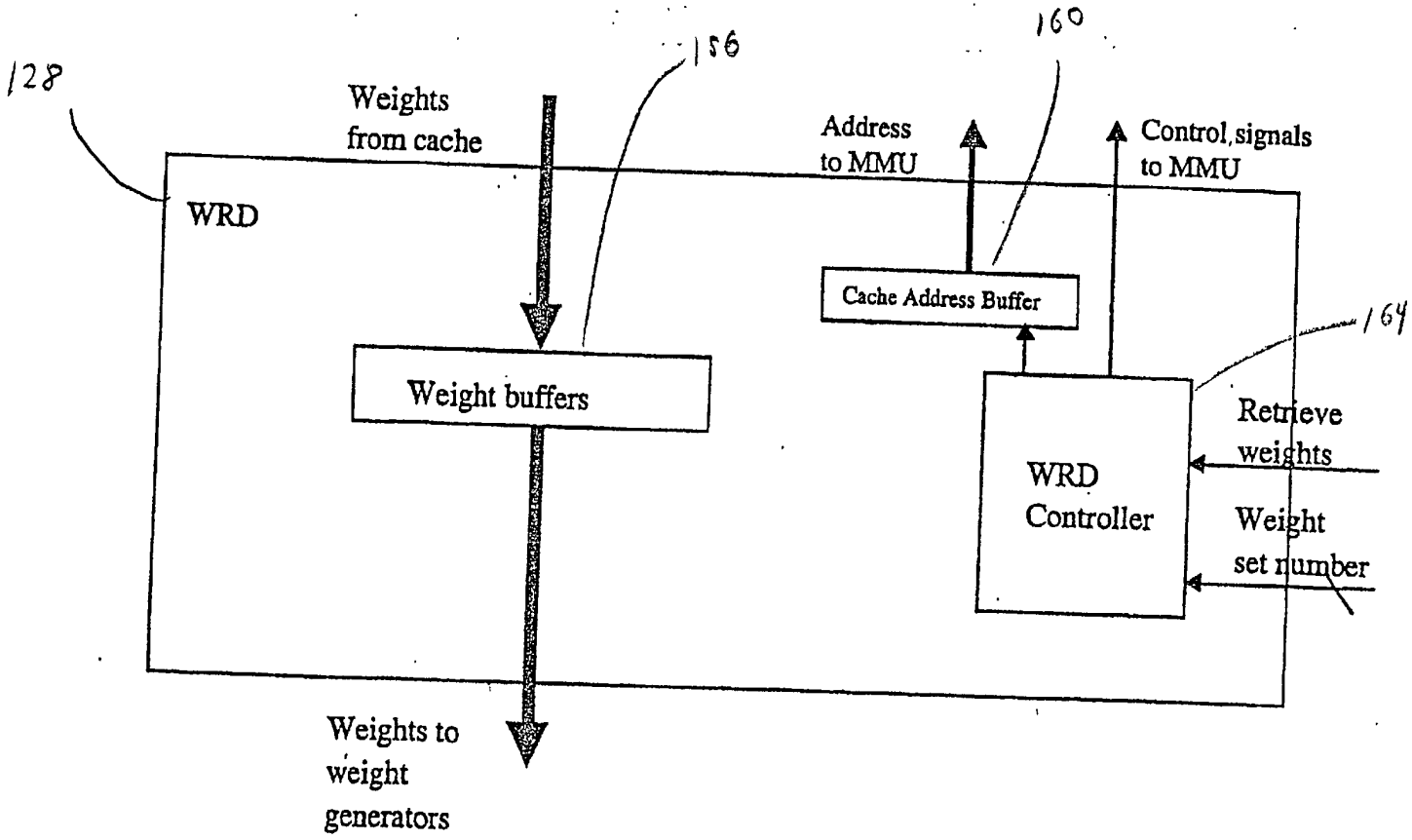
128
156
160
164
Weights from cache
Address to MMU
Control signals to MMU
WRD
Cache Address Buffer
Weight buffers
WRD Controller
Retrieve weights
Weight set number
Weights to weight generators

FIGURE 3

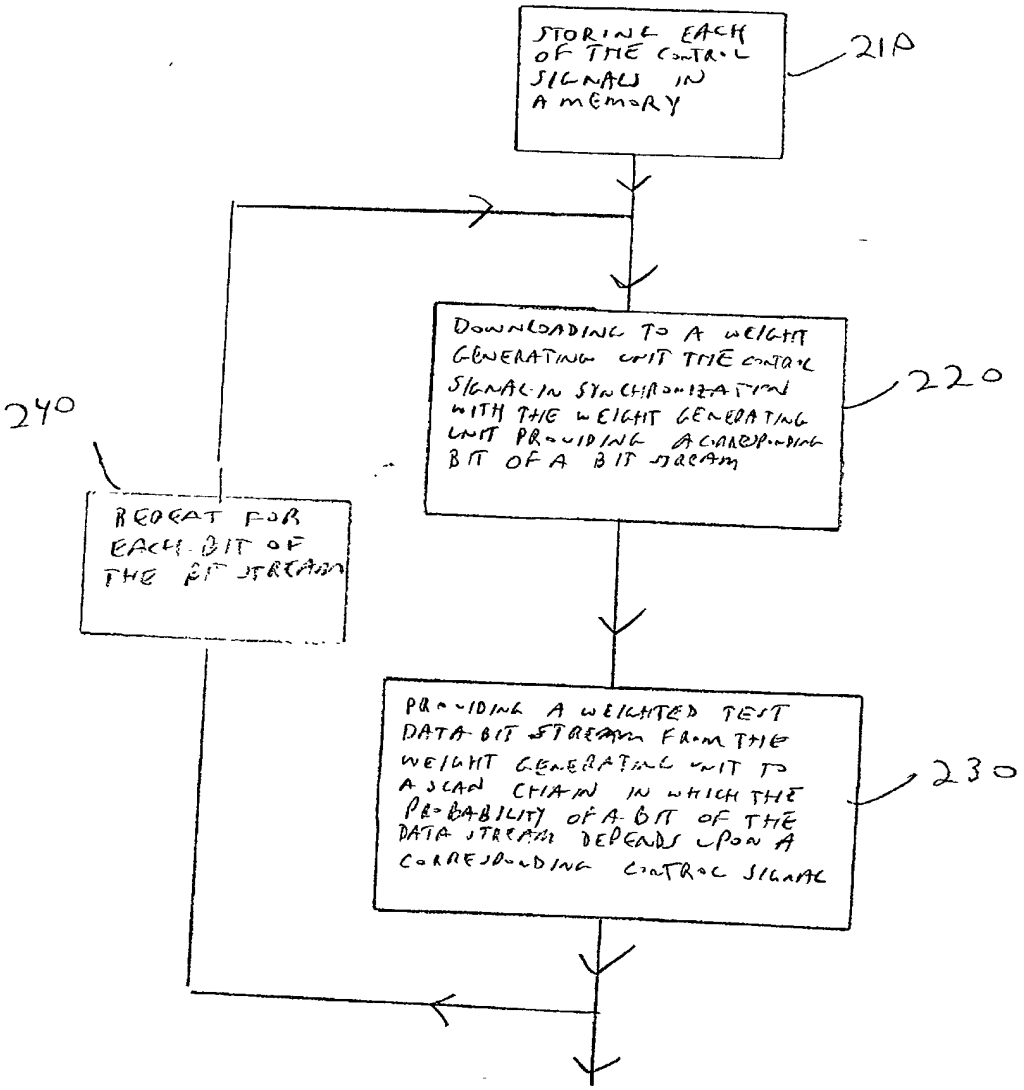
STORING EACH OF THE CONTROL SIGNALS IN A MEMORY
210
DOWNLOADING TO A WEIGHT GENERATING UNIT THE CONTROL SIGNAL IN SYNCHRONIZATION WITH THE WEIGHT GENERATING UNIT PROVIDING A CORRESPONDING BIT OF A BIT STREAM
220
240
REPEAT FOR EACH BIT OF THE BIT STREAM
PROVIDING A WEIGHTED TEST DATA BIT STREAM FROM THE WEIGHT GENERATING UNIT TO A SCAN CHAIN IN WHICH THE PROBABILITY OF A BIT OF THE DATA STREAM DEPENDS UPON A CORRESPONDING CONTROL SIGNAL
230

FIGURE 4